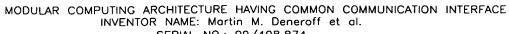
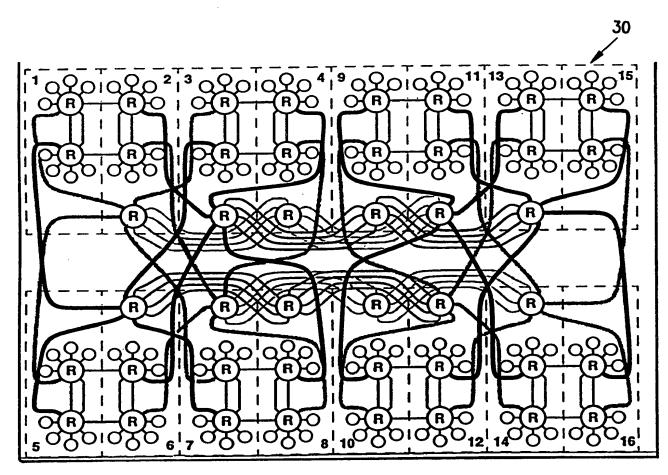


FIG. 1A



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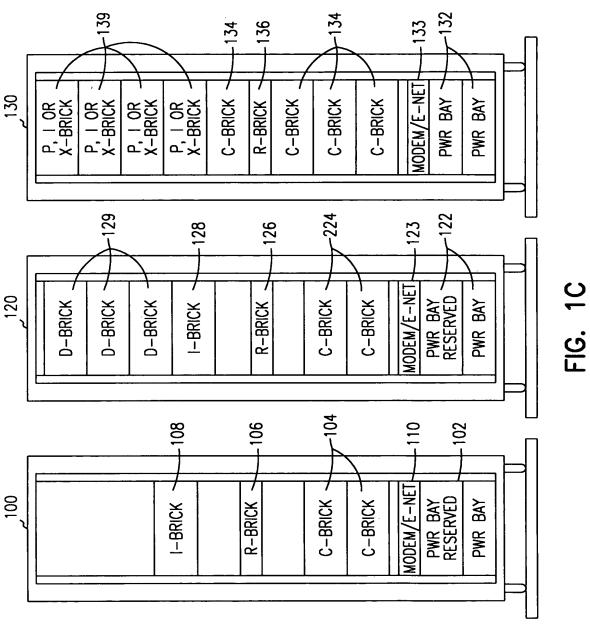


512 CPU

EXTENDED HYPERCUBE TOPOLOGY

FIG. 1B





INVENTOR NAME: Martin M. Deneroff et al. SERIAL NO.: 09/408,874 4/17 PWR BAY RESERVED R-BRICK C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK BAY R-BRICK PWR BAY R-BRICK 140 PWR C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK BA√ R-BRICK R-BRICK PWR BAY R-BRICK PWR 144 PWR BAY RESERVED C-BRICK C-BRICK C-BRICK B¥ C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK R-BRICK PWR BAY R-BRICK R-BRICK PWR MODEM/E-NET PWR BAY RESERVED C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK C-BRICK R-BRICK R-BRICK B¥ PWR BAY PWR PWR BAY RESERVED P, I OR X-BRICK P, – OR X-BRICK P, – OR X-BRICK BA√ PWR PWR BAY RESERVED P, I OR X-BRICK BAY P, – OR X-BRICK PWR

FIG.



MODULAR COMPUTING ARCHITECTURE HAVING COMMON COMMUNICATION INTERFACE INVENTOR NAME: Martin M. Deneroff et al.

SERIAL NO.: 09/408,874

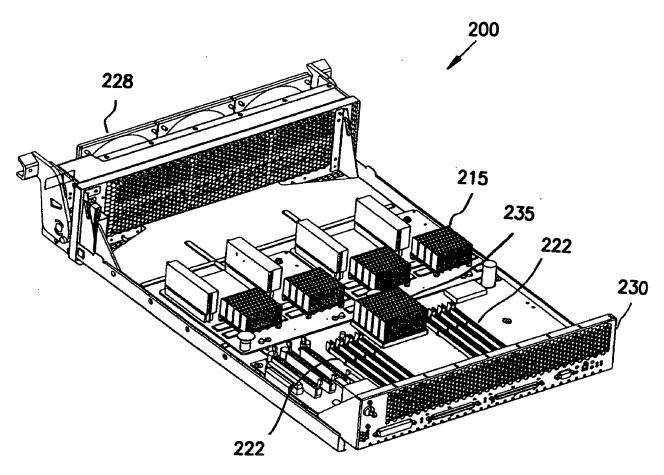
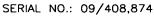


FIG. 2





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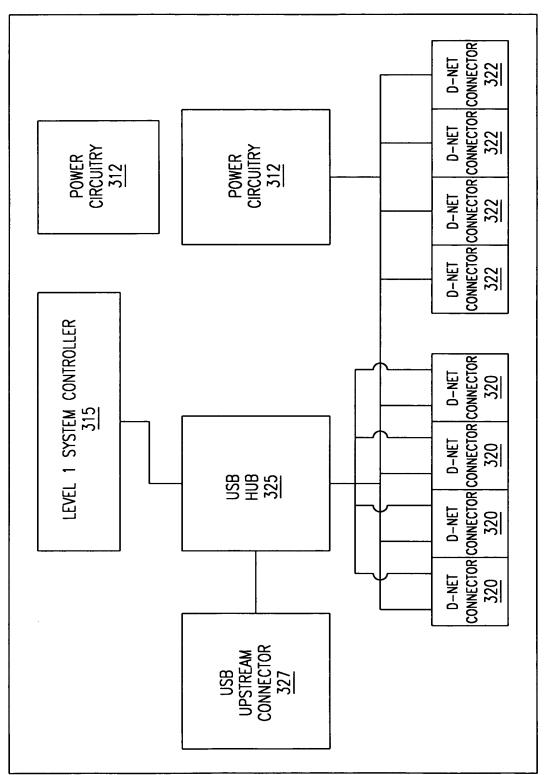


FIG. 3

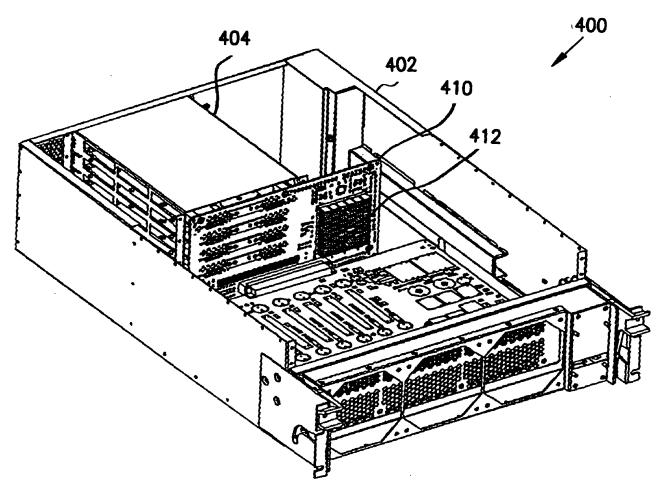


FIG. 4



TITLE: MODULAR COMPUTING ARCHITECTURE HAVING COMMON COMMUNICATION INTERFACE INVENTOR NAME: Martin M. Deneroff et al.

SERIAL NO.: 09/408,874

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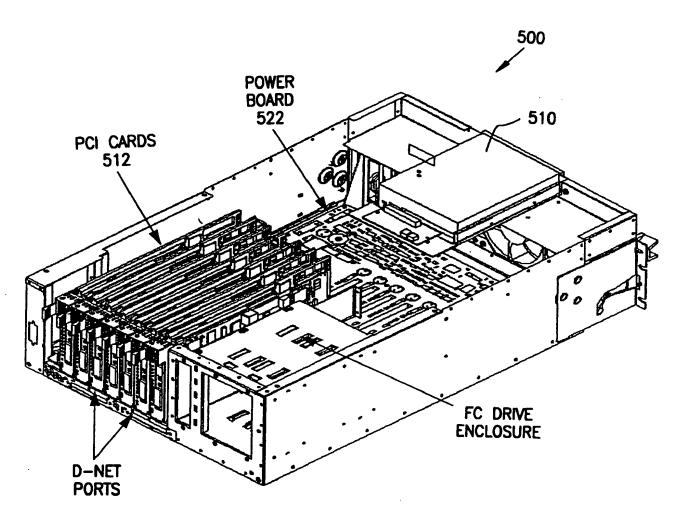


FIG. 5



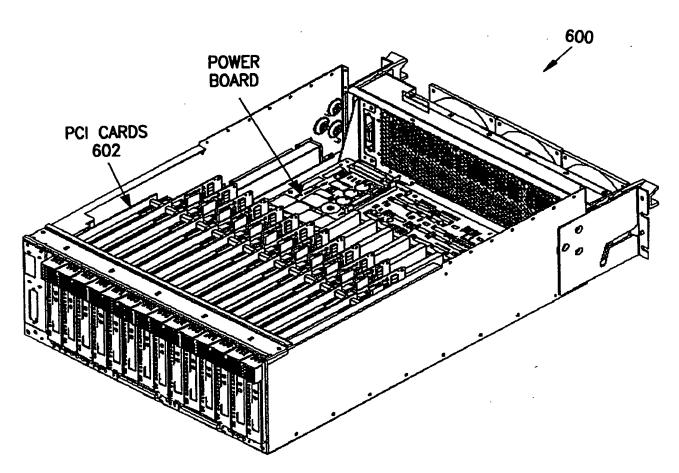


FIG. 6



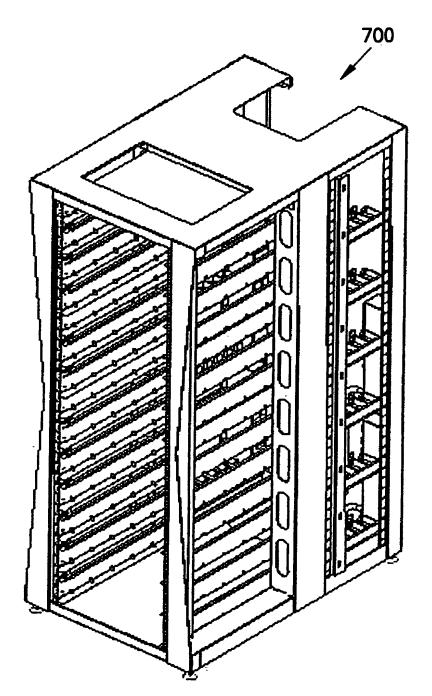


FIG. 7



THE: MODULAR COMPUTING ARCHITECTURE HAVING COMMON COMMUNICATION INTERFACE INVENTOR NAME: Martin M. Deneroff et al.

SERIAL NO.: 09/408,874

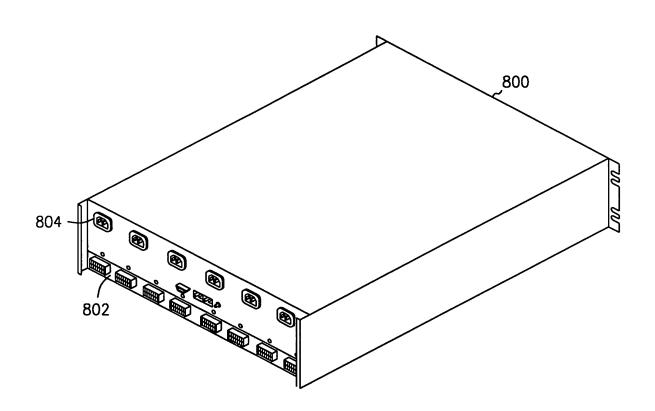
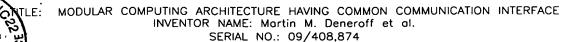
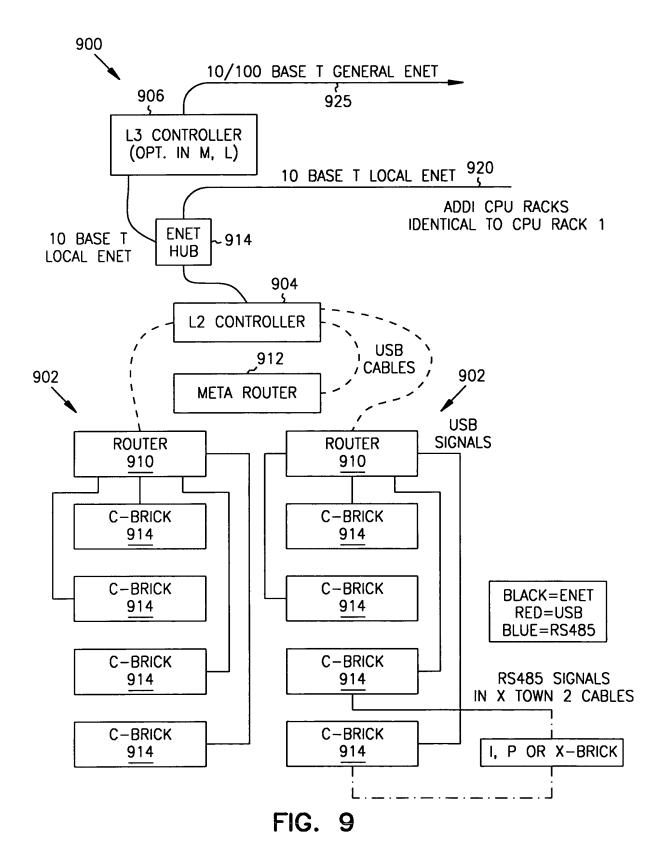


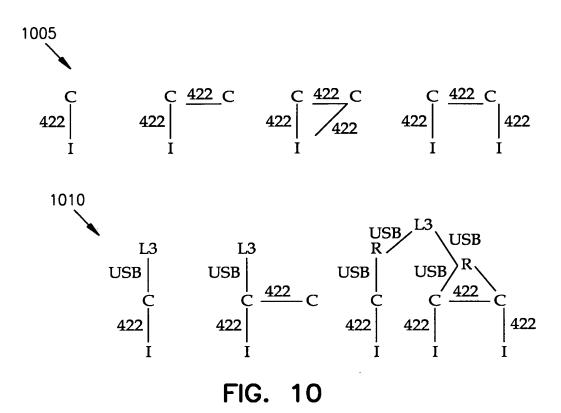
FIG. 8

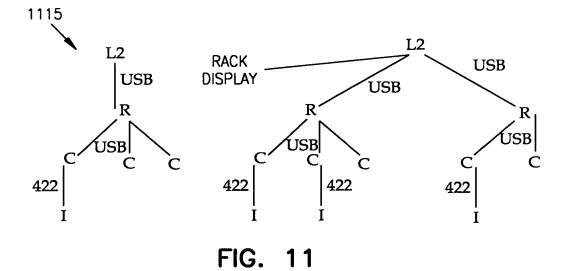












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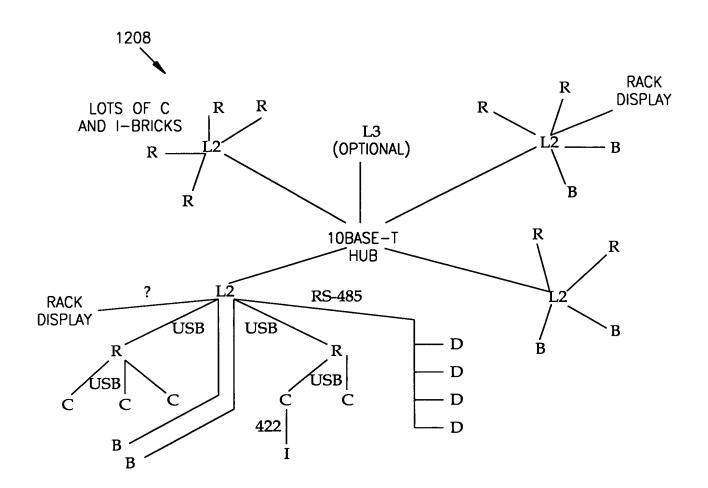


FIG. 12

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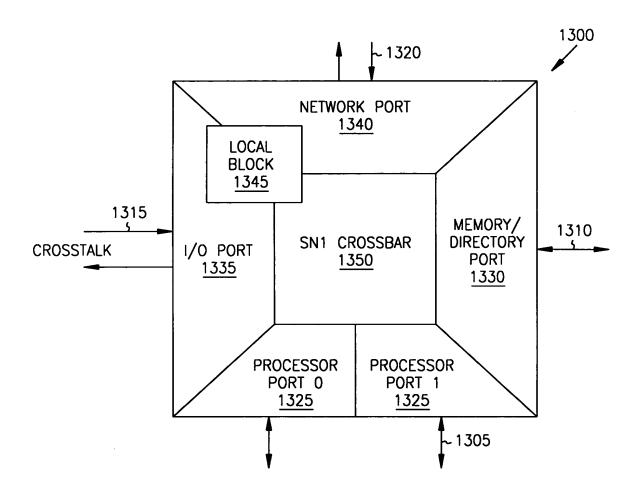


FIG. 13

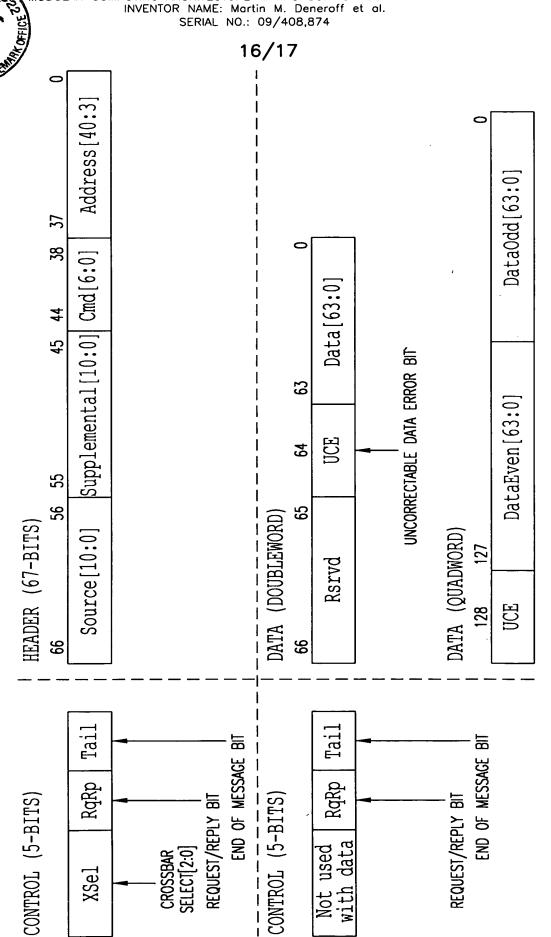


FIG. 14

SERIAL NO.: 09/408,874



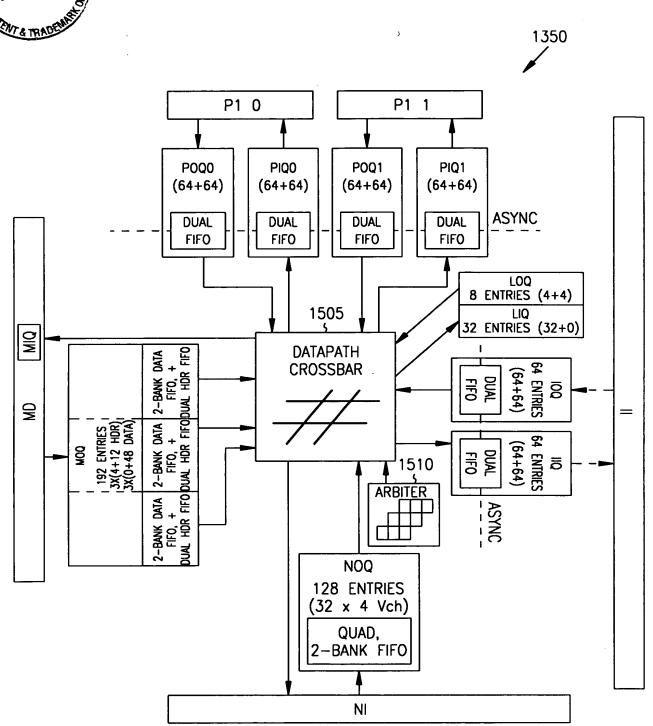


FIG. 15